

PCT/DE98/00633

Sheet 1 of 1

Form P.T.O. 1449		U.S. Department of Commerce Patent and Trademark Office			Docket No. P99,0101		Serial No. 0927367778 New Application	
LIST OF PRIOR ART CITED BY APPLICANT (use several sheets if necessary)					Applicant(s): PETER LIGGESMEYER			
					Filing Date Herewith		Group Art Unit	
U.S. PATENT DOCUMENTS								
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate	
	AA							
	AB							
	AC							
	AD							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
CSJ	AE	EP 0 424 869 A1	02/05/91	Europe				
CSJ	AF	EP 0 352 759 B1	31/01/90	Europe				
CSJ	AG	EP 0 580 663 B1	02/02/94	Europe				
CSJ	AH	EP 0 685 792 A1	06/12/95	Europe				
CSJ	AI							
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
CSJ	AJ	Burch, Jerry R. et al, "SYMBOLIC MODEL CHECKING FOR SEQUENTIAL CIRCUIT VERIFICATION", IEEE Transactions on Computer-Aided Design of Integrated Circuits and systems, Vol. 13, No. 4, April (1994), pp. 401-424						
CSJ	AK	Enders, Reinhard et al, "GENERATING BDDS FOR SYMBOLIC MODEL CHECKING IN CCS", Computer Aided Verification 3 rd , July (1991), pp. 203-213						
CSJ	AL	DIN 25424, Part 1: Fehlerbaumanalyse: Methode und Bildzeichen; Part 2: Handrechenverfahren zur Auswertung eines Fehlerebaums						
CSJ	AM	Dekleer, J. et al, DIAGNOSING MULTIPLE FAULTS, Elsevier Science Publishers, Artificial Intelligence, Vol. 32, 1987, pp. 97-130						
CSJ	AN	Nökel, K. et al, CONTROLLER SYNTHESIS AND VERIFICATION: A CASE STUDY, in: C. Leverentz, T. Lindner, Formal Development of Reactive Systems, Lecture Notes in Computer Science (No. 891), Springer 1995, pp. 55-74						
CSJ	AQ	Bryant, R., SYMBOLIC BOOLEAN MANIPULATION WITH ORDERED BINARY-DECISION DIAGRAMS, ACM Computing surveys, Vol. 24, No. 3, September (1992), pp. 293-318						
	AP							
Examiner <i>Carl Smith</i>		Date Considered <i>5/9/2002</i>						
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